

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,844,505 B1
APPLICATION NO. : 09/496960
DATED : November 30, 2010
INVENTOR(S) : Arneson et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Fig. 7, Sheet 10 of 28, for Tag “722” delete “NO?” and insert -- NO.? --, therefor.

In Column 12, Line 3, delete “a electrolytic” and insert -- an electrolytic --, therefor.

In Column 33, Line 62, delete “purchased;” and insert -- purchased, --, therefor.

In Column 40, Line 2, in Claim 2, delete “one Tag ID, each Tag ID” and insert -- a first plurality of the plurality of bits --, therefor.

In Column 40, Line 10, in Claim 4, delete “Tag ID” and insert -- first plurality of the plurality of bits corresponding to a tag --, therefor.

In Column 40, Line 14, in Claim 4, delete “a”.

In Column 40, Line 15, in Claim 4, delete “Tag ID” and insert -- first plurality of bits --, therefor.

In Column 40, Line 18, in Claim 5, delete “Tag ID” and insert -- first plurality of bits --, therefor.

In Column 40, Line 24, in Claim 7, delete “Tag ID” and insert -- of the first plurality of bits --, therefor.

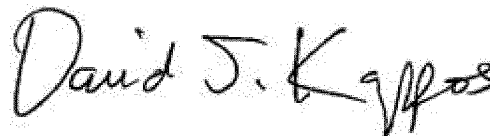
In Column 40, Line 26, in Claim 7, delete “a Tag ID” and insert -- each of the plurality of bits --, therefor.

In Column 40, Line 32, in Claim 9, delete “lease” and insert -- least --, therefor.

In Column 40, Lines 39-40, in Claim 11, delete “wherein the tag identification number includes a plurality of bits,”.

In Column 42, Line 36, in Claim 31, delete “29” and insert -- 30 --, therefor.

Signed and Sealed this
Eighteenth Day of December, 2012



David J. Kappos
Director of the United States Patent and Trademark Office